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**HAYNES AND BOONE, LLP
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MARK P. KAHLER (Depositor's name)
Mark P. Kahler (Signature)
8/15/2005 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/810,039 03/26/2004 Donald A. Kerth 23119.7 8449

TITLE OF INVENTION: SYSTEM AND METHOD FOR BIASING ELECTRICAL CIRCUITS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
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nonprovisional NO \$1400 \$300 \$1700 08/15/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
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ZWEIZIG, JEFFERY SHAWN 2816 327-530000

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1 **MARK P. KAHLER**

2 _____

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

SILICON LABORATORIES, INC. AUSTIN, TEXAS

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature *Mark P. Kahler*

Date **8/15/2005**

Typed or printed name **MARK P. KAHLER**

Registration No. **29,178**

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